

## Abstract

## Tuning circuit for a filter

5 The invention relates to a tuning circuit for tuning a filter stage, which has an RC element (1) with an RC time constant ( $\tau$ ), with the RC time constant ( $\tau$ ) being the product of the resistance of a resistor (R1) in the RC element (1) and the capacitance of a capacitor (C1),  
10 which is connected in series with the resistor (R1), in the RC element (1), having a comparator (10) for comparison of the voltage which is produced at the potential node (4) between the resistor (R1) and the capacitor (C1), with a reference ground voltage; and  
15 having a controller (15) which varies the charge on the capacitor (C1) in the RC element (1) until the comparator (10) indicates that the voltage which is produced at the potential node (4) is equal to the reference ground voltage, with the controller (15)  
20 switching a capacitor array (26) as a function of the charge variation time, which capacitor array (26) is connected in parallel with the capacitor (C1) in the RC element (1), in order to compensate for any discrepancy between the RC time constant ( $\tau$ ) of the RC element (1)  
25 and a nominal value.

(Figure 4)